



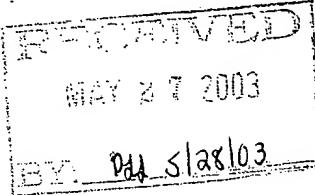
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/342,235	06/29/1999	YASUHIKO TAKEMURA	0756-1980ELE	6257

31780 7590 05/20/2003  
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EXAMINER	
SEFER, AHMED N	
ART UNIT	PAPER NUMBER

2826

DATE MAILED: 05/20/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	09/342,235	TAKEMURA, YASUHIKO
	Examiner	Art Unit
	A. Sefer	2826

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

1) Responsive to communication(s) filed on 14 March 2003.

2a) This action is **FINAL**.      2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

4) Claim(s) 6-11 and 13-25 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) 6-11 is/are allowed.

6) Claim(s) 13-25 is/are rejected.

7) Claim(s) \_\_\_\_\_ is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some \* c) None of:

1. Certified copies of the priority documents have been received.

2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.

3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 8.20.25.

4) Interview Summary (PTO-413) Paper No(s) \_\_\_\_\_.

5) Notice of Informal Patent Application (PTO-152)

6) Other: \_\_\_\_\_

**DETAILED ACTION*****Response to Amendment***

1. The amendment filed on 3/4/03 has been entered; no new claims have been added.

***Claim Rejections - 35 USC § 103***

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 13-25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tskjikawa et al. US Patent No. 5,051,570 in view of Stewart US Patent No. 5,302,966.

Tskjikawa et al disclose (see figs. 4, 6 and 9, col. 6, lines 30-33 and col. 8, lines 3-15) col. 12, lines 30-40) a semiconductor device comprising a substrate 28 having an insulating surface; at least first and second semiconductor islands 14/114 and 15/115 comprising polysilicon (as in claims 14, 17 and 21) formed directly on said insulating surface wherein each of the semiconductor islands has a channel region and a pair of impurity regions (unnumbered); a first and a second gate insulating film 134, 135 formed over said semiconductor island, respectively; at least first and second gate electrodes 112, 113 formed over said first and second semiconductor islands respectively with said first and second gate insulating films interposed therebetween; an interlayer insulating film 121 formed over a wiring (as in claims 16 and 19); a smoothing film 123 formed over a wiring (as in claims 20 and 23); a pixel electrode 124 formed over said interlayer insulating film (as in claim 16) or a pixel electrode formed over said

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smoothing film (as in claim 20) and electrically connected to one of the pair of the impurity regions (through electrode 118) of the second semiconductor island, but do not specifically disclose a wiring connecting one of the impurity regions of the NTFT first semiconductor island with the second gate electrode of PTFT second semiconductor island.

Stewart discloses in figs. 2, 3, 5 and 6 a wiring 332 connecting one of the impurity regions 226/330 of the NTFT first semiconductor island with the second gate electrode 206/334 of PTFT second semiconductor island.

Therefore, it would have been obvious to one skilled in the art at the time the invention was made to use a wiring connecting an impurity regions of one TFT with a gate electrode of another TFT, since that would enable one transistor to drive another transistor such that the data signal turns the other transistor ON thereby avoiding the formation of an extra scanning line.

As to claims 15, 18 and 22, Stewart discloses a data line 328 electrically connected to one impurity region of an NTFT.

As to claims 24 and 25, Tskjikawa et al disclose a voltage supply line 116 electrically connected to an impurity region of a semiconductor island.

*Allowable Subject Matter*

4. Claims 6-11 are allowed.

5. The following is a statement of reasons for the indication of allowable subject matter:

The prior art fails to disclose the device structure as recited in claims 6, 9, 10 and 11.

Specifically, the prior art neither teaches nor makes obvious the claimed device structures.

*Conclusion*

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

a. Yamamoto et al. US reference 5,426,526 disclose (see fig. 11) an LCD device having a plurality of transistors including a gate electrode of transistor Q1 connected to a drain of transistor Q2.

b. Unagami et al. US reference 4,528,480 disclose (see figs. 2 and 3) an AC drive EL device.

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A. Sefer whose telephone number is (703) 605-1227.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (703) 308-6601.

ANS  
May 9, 2003

Matthew J. Flynn  
EXAMINER, PATENT OFFICE  
TELEPHONE: (703) 308-6601  
